

Novel Video and Image Processing beyond Commoditization

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Commoditization of Digital Media Products





Frame Rate Conversion (FRC)

Conventional Frame Rate Conversion for DTV



FRC for Slow Motion Movie



FRC for Slow Motion Movie



More accurate motion estimation is required.

Computational Camera



Multi-View Camera Press Release(Sep. 26th, 2013)

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に入り 🌈 Two 1/4 inch 5M pix	el 1.4 µm CMOS image	🏠 🔹 🔝 🔹 🖶 🔹 ページ(P) 🗸 セーフティ(<u>s</u>) 🔹	ツール(<u>0</u>) ▼
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ISOFS	Two 1/4 inch 5M pixel 1.4	µm CMOS image sensors: TCM9518	MD
MOS Area Image Sensors			
	The TCM9518MD camera module incorporates	two 1/4-inch 5M-pixel CMOS image	E .
near Image Sensors	sensors, making it suitable for smartphone and	tablet PC applications. The distance	
	between the camera and objects can be calcu	lated by using the TCM9518MD in	
agnetic Sensors	combination with a dedicated companion LSI.	As a result, distance information can be	
New Products	provided as depth data, which allows you to c	reate new features together with	
	application software. For example, it is possible	e to realize a focus adjustment feature and	
ocuments	objects extraction after images are captured. The resolution enhancement reature		
	approximately the same level as 13M-pixel small	artohone cameras. As a result, it is possible	
ecommended Products by	to realize smartphones that are thinner than those with a 13M-pixel camera but offer		
pplication	comparable resolution.		
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ews Release/Topics	Eastures (Medule + Companier		
MOS Image CIRCUS		two 5Mp	CISs
-	Applications		0103
	 Mobile phones and Smart phones 	Optical format: 1/4	
	PCs and tablet PCs	Pixel size: 1.4 µm × 1.4 µm	
		Effective pixels: 2596 (H) × 1948 (V) × 2	2

Array Camera Module and Companion ASIC



- Two 5Mp (1.4um) and companion ASIC
- 13M high resolution image output and VGA depth map output at 24fps
- Fast digital auto-focus feature without mechanical VCM
- Low height: 4.65mm

<Spec. of Companion ASIC>

Process	40nm CMOS
Package Size	6mm x 6mm
Input	5M pix. x 2 ch.
Output	13M pix (Digital Focus, Deep Focus) + VGA depth map
I/Os	MIPI CSI2 2-lane x 2 (In), MIPI CSI2 4- lane x 1 (Out), I ² C

Refocus Processing using Depth Information



4D Recursive Search

Takuma Yamamoto, et. al, "High-Accuracy Motion Estimation with 4-D Recursive Search Block Matching," GCCE, 2012



4D = 3D (2D + hierarchical search) + time

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Image Recognition SoC Family Roadmap

