SILEXICA

Multicore use cases in automotive

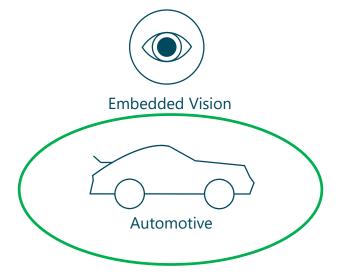
Weihua Sheng MPSOC 2017

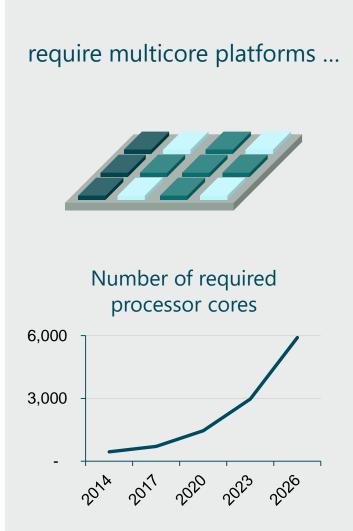
Problem

Demanding applications...

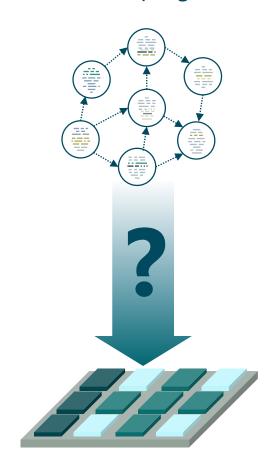


5G connectivity





that are hard to program efficiently



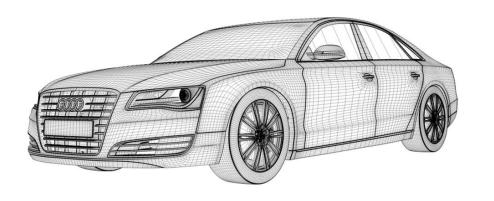
Multicore Challenges in automotive industry

1. "Classical" ECU



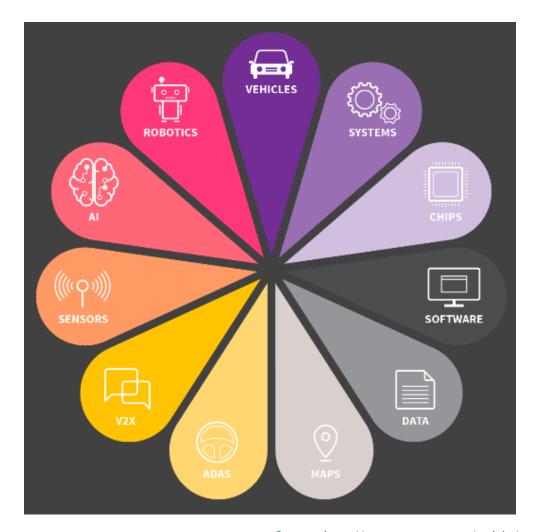
- ✓ Defines a layered software architecture
- ✓ Offers multiple levels of abstraction
- ✓ Defines a methodology
- ✓ Supports multiple cores with version 4.0

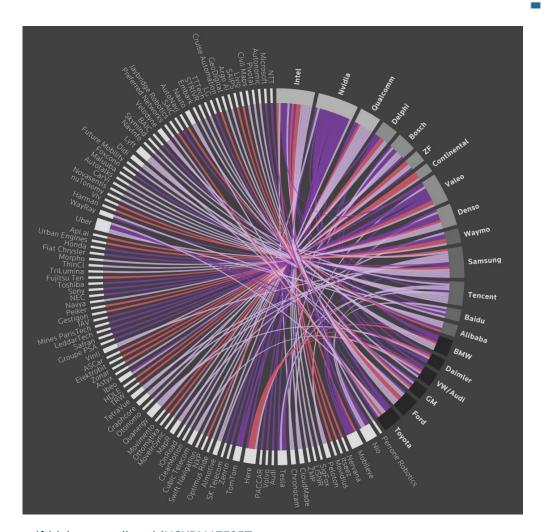
The AUTOSAR Classic Platform is used for automotive control applications



Multicore Challenges in automotive industry

2. "New guy in town" autonomous driving



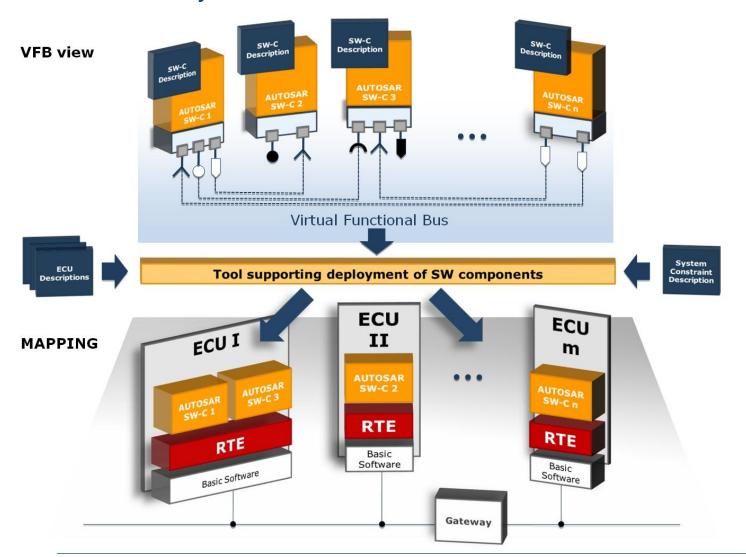


Source: http://www.reuters.com/article/us-autos-selfdriving-suppliers-idUSKBN17F0EZ



INTRODUCTION

AUTOSAR System Overview



VFB Virtual Functional Bus

Can be considered as a virtual communication interface

RTE Runtime Environment

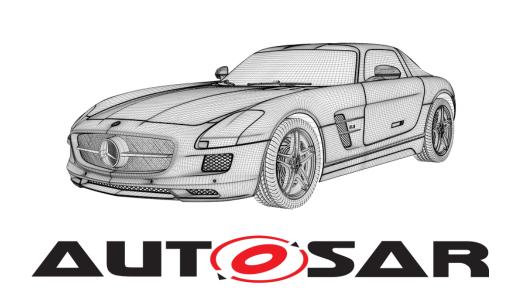
 Can be considered as the implementation of the VFB for a specific ECU

INTRODUCTION

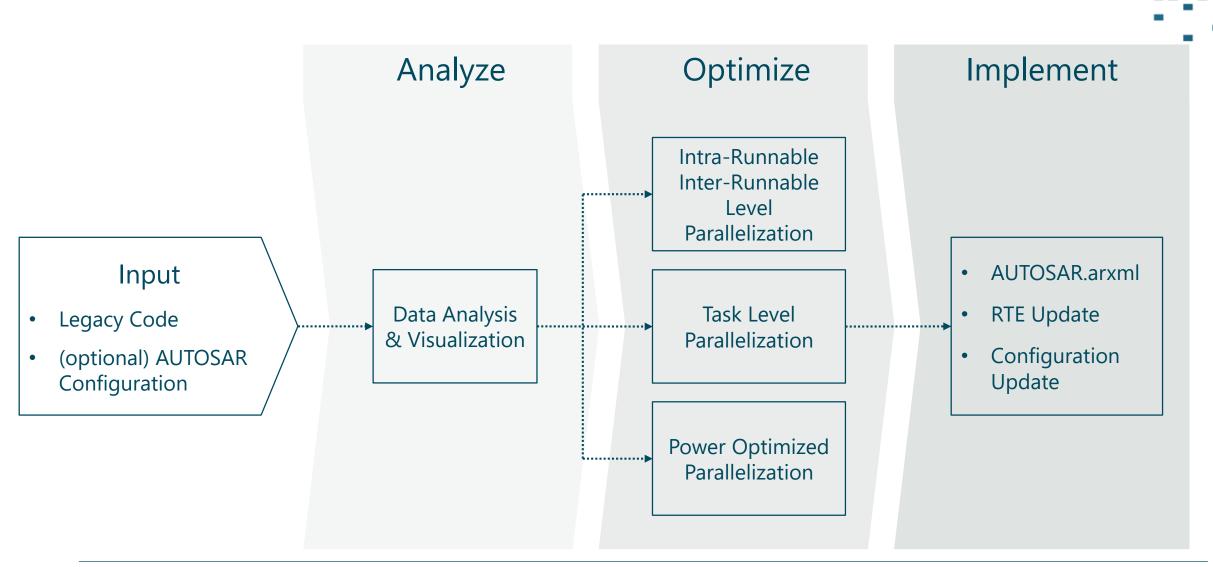
AUTOSAR Classic Platform

AUTOSAR Facts

- Runnables are executable entities
- Runnables are mapped to tasks
- Tasks are the scheduled items
- Tasks are bound to a single core
- OS Applications define the task to core mapping



SLX Tool Suite - Automotive

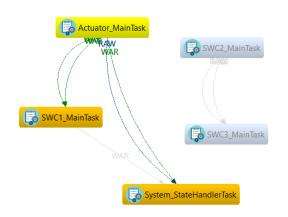


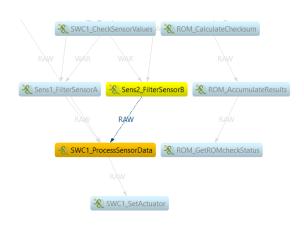
ANALYZE

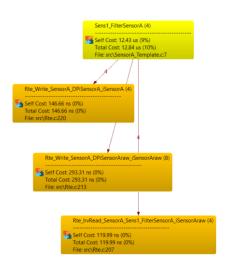
Automotive Flow

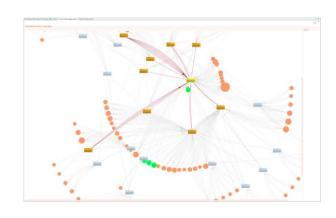
Different level of abstraction with focus on:

- Task level data analysis
- Runnable level data analysis









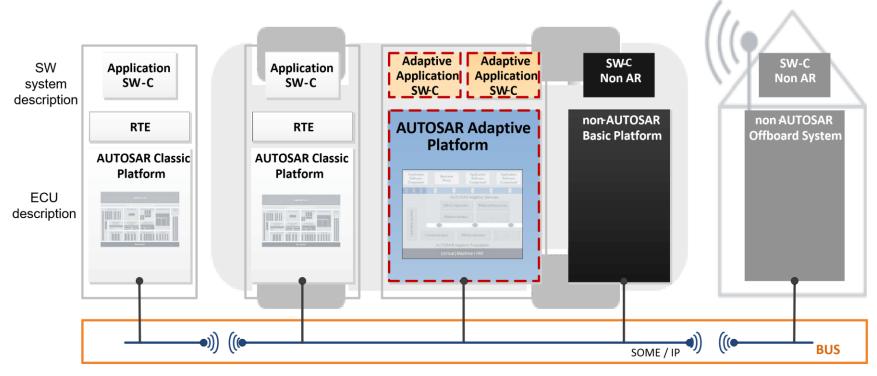
Inter-Task Data Dependencies Inter-Runnable Dependencies

Intra-Runnable Call Graph Code Analysis Graph

OUTLOOK

A new AUTOSAR standard: The AUTOSAR Adaptive Platform

The new platform will close the gap. It will support adaptive deployment, complex microcontrollers and interaction with non AUTOSAR systems.



Common Bus Interface Specification

