

## Audio Subsystem Solutions for Consumer SoCs

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MPSoC 2011

July 4-8, 2011

### Trends in audio

#### Internet-enabled devices

→ Support many audio formats



#### Multi-channel audio content

→ Support multi-channel audio formats



#### HD audio content

→ High sampling rates, 24-bit precision

1x

#### Sound processing functions

→ Algorithms for improved audio quality

16-bit  
Stereo  
44.1kHz

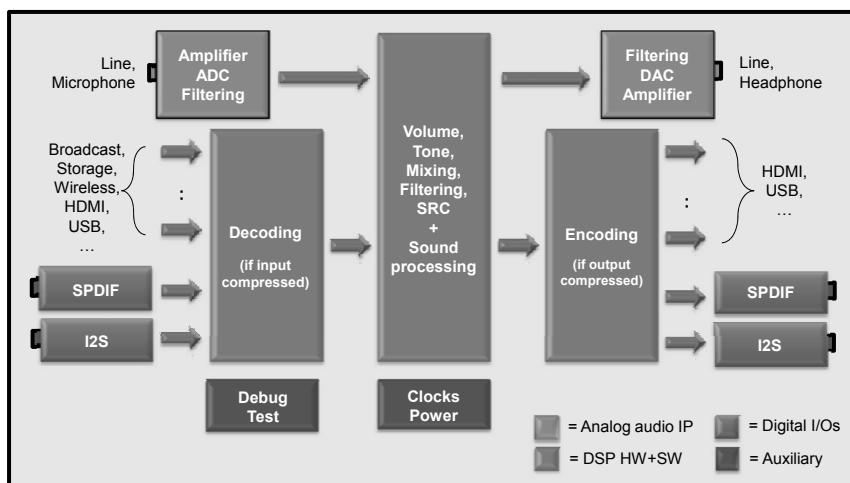


- Not a side activity on host processor
- Off-load to separate audio DSP

## Outline

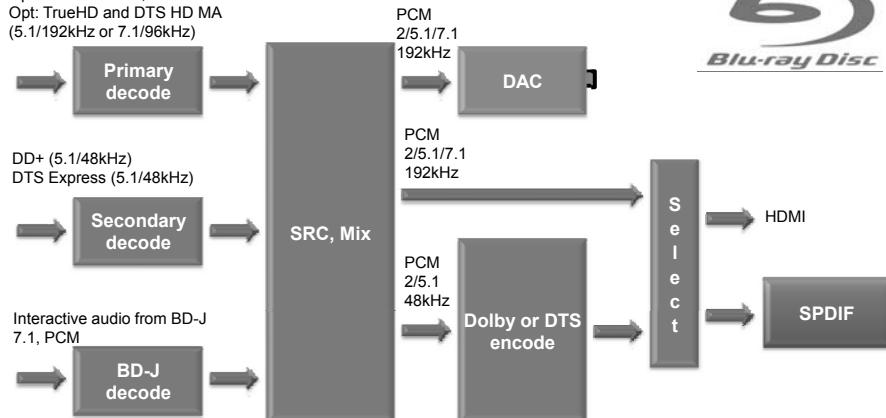
- Audio subsystem solutions
  - Building audio subsystems
  - Integrating audio subsystems
  - Reusing audio subsystems
- Conclusions

## Audio processing



## Use case Blu-ray playback

Mandatory: LPCM, DD, DTS  
 Opt: DTS HD HRA, DD+  
 Opt: TrueHD and DTS HD MA  
 (5.1/192kHz or 7.1/96kHz)

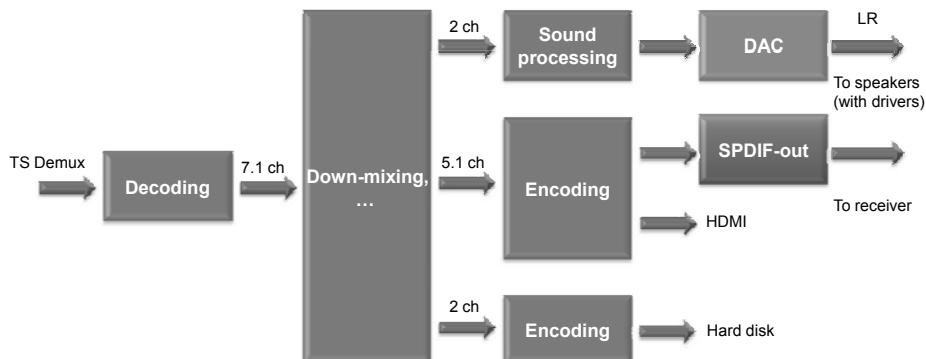


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Predictable Success

## DTV watch and record same channel



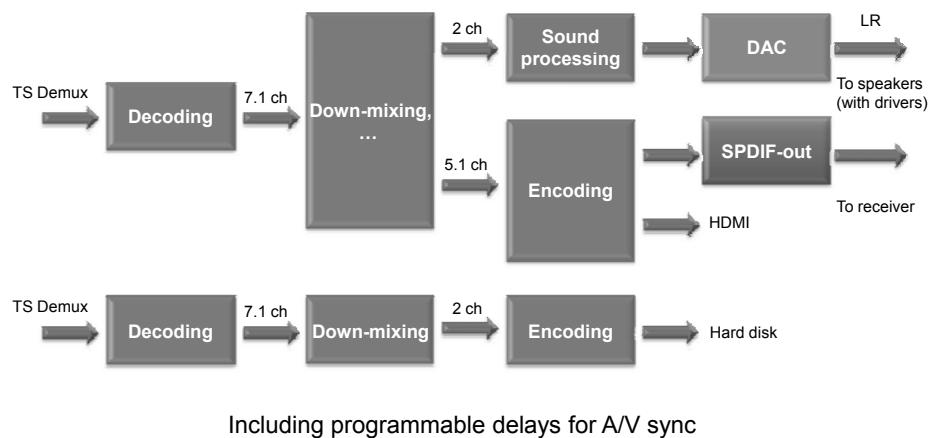
Including programmable delays for A/V sync

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## DTV watch and record different channels

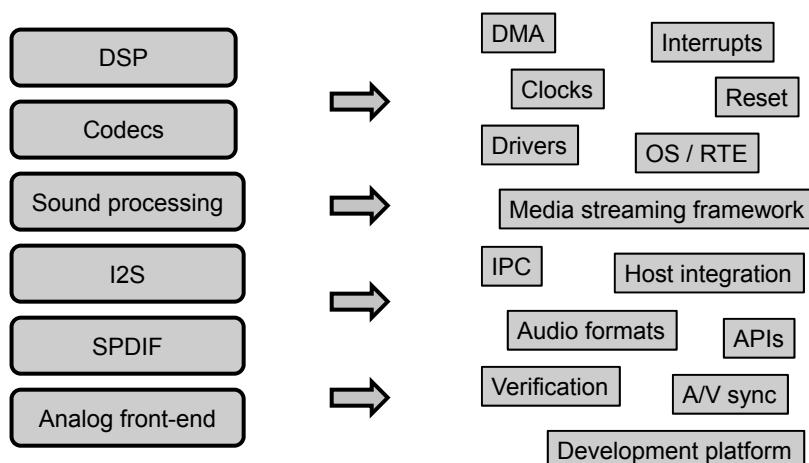


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## Building an audio solution from IP



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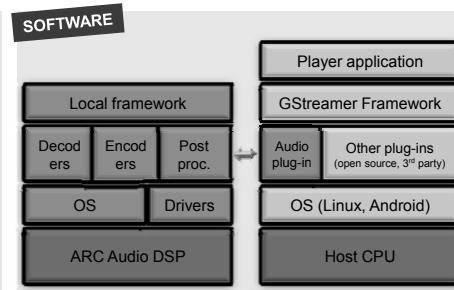
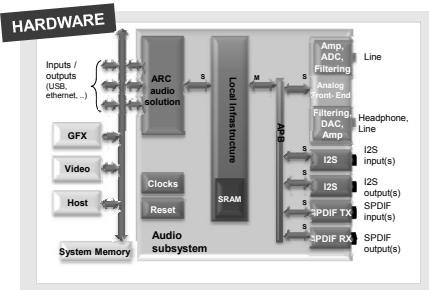
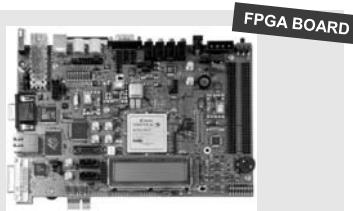
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## Alternative: audio subsystem

### Complete Audio Solution:

- Software + Hardware
- Codecs + Sound Processing
- Digital + Analog Peripherals
- Host Plug-in Software
- Development Platform



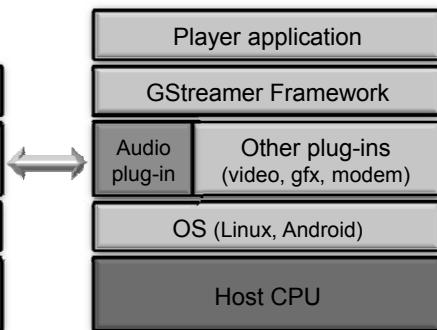
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## Host integration

- SW plug-in on Host CPU with standard API
- Audio functions visible on Host
- Build and execute use cases on Host (incl. audio, video, etc.)



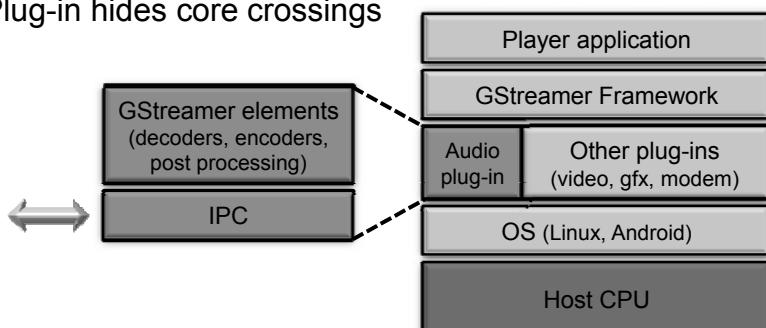
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## Host integration, SW plug-in

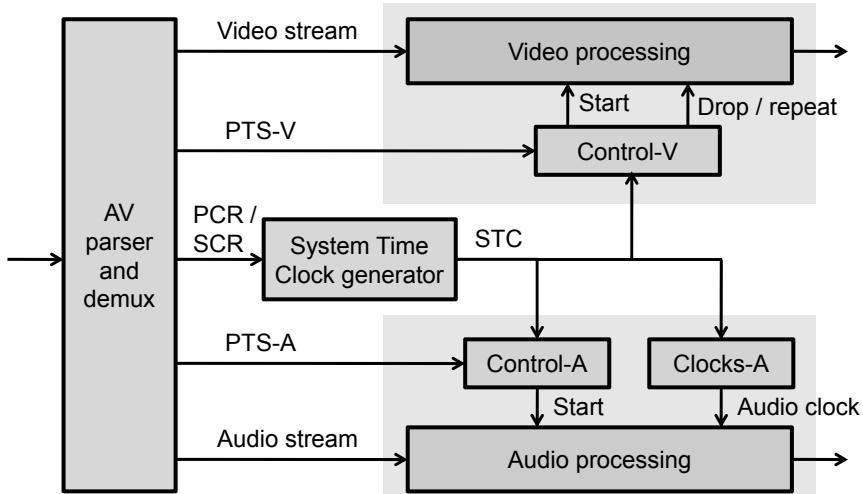
- SW plug-in implements small stubs for audio functions
- Audio functions can be created, started, stopped, etc.
- Audio functions can be hooked up in use cases
- Plug-in hides core crossings



## Sample clocks

Optical disc		HDD / SSD	No sample clock needed • E.g. ripping • No use of audio peripherals
Optical disc		I2S / SPDIF	Master • E.g. playback from storage • Generate sample clock(s) • Use of audio peripherals
I2S / SPDIF		I2S / SPDIF	Slave to external clock • E.g. playback from I2S / SPDIF • Use external sample clock(s)
Broadcast		I2S / SPDIF	Slave to transmitted clock • E.g. broadcast playback • Reconstruct sample clock(s) • Synchronize to transmitted clock

## A/V synchronization (lip sync)



## Conclusions

- Audio subsystems have added value over a “bag of IP”
  - HW-SW integration, Clocks, Host integration
- Audio subsystem reuse
  - Instances derived from configurable template
  - Just like configurable IP
- Audio subsystems from external suppliers
  - Reduce development effort
  - Free-up engineering resources
  - Reduce time to market