



MPSOC '14

14th International Forum on Embedded MPSoC and Multicore

July 7 - 11, 2014, Relais de Margaux, Margaux, France

FOCUS

MPSOC is an interdisciplinary forum bringing together key R&D actors from the different fields required to design embedded multiprocessor SoC (MPSoC) and Multicore. Thanks to its full week format and the high quality of both attendees and speakers, MPSOC is a unique opportunity for executives and senior managers to explore new ideas and refine strategic thinking.

CONTENTS

The program brings together key actors from IP, fabless, semiconductor, system houses and design industries to build a vision of the next step in integrated system design. More than 50 world class R&D speakers will discuss **fundamental and strategic issues to master embedded MPSoC and multicore design.**

The program includes keynotes on major MPSoC trends and technical sessions to present strategic directions and state-of-the-art research. The 5-day program will consist of in-depth technology challenge presentations, and short keynotes followed by insightful panels. All the talks will be given by CTO-level speakers.

The main domains addressed at MPSoC are among others Big Data, High Performance Computing, Manycore architectures, MPSoC key applications, smart sensors, energy efficiency techniques, MPSoC networks, design methodology, virtual prototyping, graphics and video processing, and parallel programming models and tools.

WHY ATTEND

MPSOC features an excellent technical program with internationally **recognized expert speakers and top executives.** It is **the single best event in the world** that brings together so many leading thinkers on the future of SoC design. MPSoC enables **great informal networking and interactions** with experienced, distinguished researchers and top academic and industrial experts in MPSoC. It builds **bridges between different technical areas and corporations/institutes/countries.** Finally, it is a unique environment for anyone who wants to share knowledge with researchers and key managers from industry.

FEE

For registration before May 15, the fee amounts to 1400 USD for regular attendees, 1190 USD for IEEE and EDAA members, and 790 USD for students and speakers. It will cover the documentation, the lunch for five days, dinner for four days including the social dinner. 2 days registration before May 15 amounts to 790 USD.

FOR REGISTRATION, ACCOMMODATION, VENUE, and FURTHER INFORMATION, PLEASE CONTACT:

Suzanne Lesecq, CEA-Leti, France / Raphaël David, CEA-List, France – Email: mpsoc2014@imag.fr

Further information is available via:

<http://www.mpsoc-forum.org>

Sponsored by

European Design and Automation Association, IEEE Council on Electronic Design Automation, and IEEE Circuits and Systems Society, CEA-List, CEA-Leti, TIMA

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Marilyn Wolf, Georgia Institute of Tech., USA
Masaharu Imai, Osaka Univ., Japan
Hiroto Yasuura, Kyushu Univ., Japan

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General Co-Chair Europe:

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Yoshinori Takeuchi, Osaka University, Japan
Hiroyuki Tomiyama, Ritsumeikan Univ., Japan
Kees Vissers, Xilinx, USA
Norbert Wehn, Univ. of Kaiserslautern, Germany
Marilyn Wolf, Georgia Institute of Tech., USA
Yuan Xie, Penn State/AMD Research, USA
Hiroto Yasuura, Kyushu University, USA
Sungjoo Yoo, Postech, Korea

KEYNOTES

Alexis Mather, ARM, UK
 Giorgio Cesana, ST Microelectronics, France
 Akihiko Shinya, NTT Nanophotonics Center, Japan
 Sylvain Martel, École Polytechnique de Montréal, Canada

IN-DEPTH PRESENTATIONS

K. Charles Janac, Arteris Inc., USA
 Jim Tung, Mathwork, USA
 Rishiyur Nikhil, BlueSpec, USA
 Yankin Tanurhan, Synopsys, USA
 Manju Hegde, AMD, USA
 Kees Vissers, Xilinx, USA
 John Goodacre, ARM, UK
 Patrick Blouet, ST Microelectronics, France
 Arnaud Grasset, Thales RT, France
 Jean Pierre Tual, Gemalto, France
 Jochen Haerdlein, Bosch, Germany
 Giampietro Tecchiolli, Eurotech, Italy
 Tsuneo Nakata, Fujitsu Lab. of Europe, Japan
 Yoshifumi Sakamoto, IBM, Japan
 Jason Cong, UCLA, USA
 James C. Hoe, Carnegie Mellon University, USA
 Marilyn Wolf, Georgia Tech, USA
 Hironori Kasahara, Waseda University, Japan
 Babak Falsafi, EPFL, Switzerland
 Sorin Cotofana, TUD, The Netherlands

MINI-KEYNOTES

Akira Asato, Fujitsu, Japan
 Koichiro Yamashita, Fujitsu, Japan
 Takashi Miyamori, Toshiba, Japan
 Yukoh Matsumoto, TOPS Systems, Japan
 Masaki Gondo, eSOL, Japan
 Shinobu Fujita, Toshiba, Japan

Yuichi Nakamura, NEC, Japan
 Soojung Ryu, Samsung Advanced Institute of Tech., Korea
 Chinyun Kim, Samsung, Korea
 Marcello Coppola, ST Microelectronics, France
 Frederic Rousseau, TIMA, France
 Kees van Berkel, ST Ericsson/TUE, The Netherlands
 Pieter van der Wolf, Synopsys, The Netherlands
 Yuan Xie, Penn State/AMD Research, USA
 Michel Laurence, ETS/Octasic, Canada
 Pierre G. Paulin, Synopsys, Canada
 Keiji Kimura, Waseda University, Japan
 Nakajima Masaitu, Panasonic, Japan
 Koji Inoue, Kyushu University, Japan
 Hiroyuki Tomiyama, Ritsumeikan University, Japan
 Tsuyoshi Isshiki, Tokyo Institute of Technology, Japan
 Masaaki Kondo, The University of Electro-Com., Japan
 Yoshinori Takeuchi, Osaka University, Japan
 Jiang Xu, Hong Kong Univ. of Science and Tech., Hong-Kong
 Sungjoo Yoo, POSTECH, Korea
 Xiaoyao Liang, Shanghai Jiao Tong University, China
 Rui Hou, Chinese Academy of Sciences, China
 Youn-Long Lin, National Tsing Hua University, Taiwan
 Emil Matus, Technical University Dresden, Germany
 Gerd Ascheid, RWTH Aachen University, Germany
 Norbert Wehn, University of Kaiserslautern, Germany
 Andreas Herkersdorf, TU Munich, Germany
 Gerhard P. Fettweis, TU Dresden, Germany
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 Raphaël David, CEA LIST, France
 Marc Polina, M3 Systems, France
 Giovanni Beltrame, École Polytechnique de Montréal, Canada
 Gabriela Nicolescu, Ecole Polytechnique de Montréal, Canada
 Fumio Arakawa, Nagoya University, Japan
 Kees Goossens, TUE, The Netherlands

MPSOC'14 FORUM REGISTRATION FORM

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Please mail or fax this form to: MPSOC'14 – MP Associates, 5405 Spine Road, Suite 102, Boulder, CO 80301 - USA
 Tel: +1 (303) 530-4562 Fax: +1 (303) 530-4334

First Name _____ Last Name _____ Company _____
 Address 1 _____ City _____ State _____ Zip _____
 Country _____ Phone _____ Fax _____ Email _____

ADVANCE REGISTRATION (Postmarked by May 15, 2014)

	Regular attendee	IEEE/EDAA member	Student	Speaker	2 Days
Advance Registration	1400 USD	1190 USD	790 USD	790 USD	790 USD
After May 15	1700 USD	1400 USD	1190 USD	1190 USD	1190 USD

TOTAL FEES _____

For 2 days only, please circle one: Mon/Tue Tue/Wed Wed/Thur Thur/Fri

Send full payment in U.S. dollars with this form. Use a check drawn on a US bank or a major credit card. For payments from non-U.S. banks the attendee will be charged a collection fee of US\$30.00. Purchase orders are not accepted. Make checks payable to MPSOC'14. Use your credit card if registering by fax.

check ___ credit card ___ Visa ___ Mastercard ___ American Express ___ Card # _____ Exp. date _____
 Name(as it appears on card) _____ Signature _____

REFUNDS: Requests for refunds received before May 15, 2014 will be subjected to a \$50 processing fee. No refunds will be made for cancellations received after May 15, 2014 and all registration fees will be forfeited. Register early to avoid disappointment.